

MAY. 11. 2007 10:46AM
TO: USPTO

ZILKA-KOTAB, PC

NO. 6035 P. 1

RECEIVED
CENTRAL FAX CENTER
MAY 11 2007

ZILKA-KOTAB

PC

100 Park Center Plaza, Suite 300
San Jose, CA 95113
Telephone (408) 971-2573
Facsimile (408) 971-4660

May 11, 2007

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Title: Method and Apparatus for Synchronizing Data from Asynchronous Disk Drive Data Transfers
Serial No. 10/822,115
Filing date: 04/08/2004

Dear Sir:

Please enter the enclosed Revocation and Power of Attorney and Statement Under 37 CFR 3.73(b) forms into the file of the referenced application.


Very truly yours,
Zilka-Kotab, PC

Kevin J. Zilka
Reg. No. 41,429

Enclosure

CERTIFICATE OF FACSIMILE

I hereby certify that this correspondence is being facsimile transmitted to the Commissioner of Patents at facsimile number: (571)273-8300 on May 11, 2007.


Erica L. Farlow

5/11/07
Date

-1-

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Title: Multi-Platter Disk Drive Controller and Methods for Synchronous Redundant Data Operations

Serial No. 11/266,764

Filing date: 11/02/2005

Title: Intelligent Storage Engine for Disk Drive Operations with Reduced Local Bus Traffic

Serial No. 11/316,436

Filing date: 12/22/2005

Title: Disk Controller Methods and Apparatus with Improved Striping, Redundancy Operations and Interfaces

Serial No. 11/080,376

Filing date: 03/14/2005

Title: Disk Array Controller with Reconfigurable Data Path

Serial No. 10/829,918

Filing date: 04/21/2004

Title: Method and Apparatus for Synchronizing Data from Asynchronous Disk Drive Data Transfers

Serial No. 10/822,115

Filing date: 04/08/2004

Title: Raid Controller Disk Write Mask

Serial No. 11/060,884

Filing date: 02/17/2005

Title: Raid Controller Disk Write Mask

Serial No. 10/267,928

Patent No. 6,904,498

Filing date: 10/08/2002

Issue date: 06/07/2005

Title: ATA Emulation Host Interface in a RAID Controller

Serial No. 10/885,960

Filing date: 07/06/2004

Title: Disk Array Controller For Reading/Writing Striped Data Using A Single Address Counter For Synchronously Transferring Data Between Data Ports And Buffer Memory

Serial No. 08/642,453

Patent No. 6,018,778

Filing date: 5/3/1996

Issue date: 1/25/2000

-2-

Title: On-The-Fly Redundancy Operation For Forming Redundant Drive Data And
Reconstructing Missing Data As Data Transferred Between Buffer Memory And Disk Drives
During Write And Read Operation Respectively

Serial No. 09/411,698

Filing date: 10/1/1999

Patent No. 6,237,052

Issue date: 5/22/2001

RECEIVED
CENTRAL FAX CENTER
MAY 11 2007

Title: Raid Controller System and Method With ATA Emulation Host Interface

Serial No. 09/667,965

Filing date: 9/21/2000

Patent No. 6,772,108

Issue date: 8/3/2004

Title: On-The-Fly Redundancy Operation For Forming Redundant Drive Data And
Reconstructing Missing Data As Data Transferred Between Buffer Memory And Disk Drives
During Write And Read Operation Respectively

Serial No. 10/445,396

Filing date: 05/22/2003

REVOCATION OF PRIOR POWERS OF ATTORNEY
AND GRANT OF NEW POWER OF ATTORNEY

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

As assignee of record of the entire interest of the above-identified
application(s)/patent(s), all powers of attorney previously given are hereby revoked and the
following agents are hereby appointed to prosecute and transact all business in the U.S. Patent
and Trademark Office connected therewith.

Kevin J. Zilka (Reg. No. 41,429);
Dominic M. Kotab (Reg. No. 42,762);
Richard B. Domingo (Reg. No. 36,784);
Joseph Vo (Reg. No. 43,971); and
T. Rao Coca (Reg. No. 29,784).

Please send all correspondence for these application(s)/patent(s) as follows:

Customer Number: 28875
Zilka-Kotab, PC
P.O. Box 721120
San Jose, California 95172-1120

Please direct any calls to Kevin Zilka at (408) 505-5100.

Date 5/10/07

Richard B. Domingo
Signed by: Richard B. Domingo

Title: Director of Intellectual PropertyAssignee of Record: NVIDIA Corporation

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

Approved for use through 09/30/2007. OMB 0851-0031
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

STATEMENT UNDER 37 CFR 3.73(b)Applicant/Patent Owner: NetCell Corp.Application No./Patent No.: "See Appendix A (attached)" Filed/Issue Date: "See Appendix A (attached)"Entitled: "See Appendix A (attached)"

Nvidia Corporation _____ a Corporation

(Name of Assignee)

(Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

1. ☒ the assignee of the entire right, title, and interest; or
2. ☐ an assignee of less than the entire right, title and interest
(The extent (by percentage) of its ownership interest is _____ %)

In the patent application/patent identified above by virtue of either:

A. ☒ An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel _____, Frame _____, or for which a copy thereof is attached.

OR

B. ☐ A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as follows:

1. From: _____ To: _____
The document was recorded in the United States Patent and Trademark Office at Reel _____, Frame _____, or for which a copy thereof is attached.
2. From: _____ To: _____
The document was recorded in the United States Patent and Trademark Office at Reel _____, Frame _____, or for which a copy thereof is attached.
3. From: _____ To: _____
The document was recorded in the United States Patent and Trademark Office at Reel _____, Frame _____, or for which a copy thereof is attached.

☐ Additional documents in the chain of title are listed on a supplemental sheet.

☒ As required by 37 CFR 3.73(b)(1)(i), the documentary evidence of the chain of title from the original owner to the assignee was, or concurrently is being, submitted for recordation pursuant to 37 CFR 3.11.

[NOTE: A separate copy (i.e., a true copy of the original assignment document(s)) must be submitted to Assignment Division in accordance with 37 CFR Part 3, to record the assignment in the records of the USPTO. See MPEP 302.08]

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

Richard B. Domingo

Signature

Richard B. Domingo

Printed or Typed Name

Director of Intellectual Property

Title

5/10/07

Date

408-566-6491

Telephone Number

This collection of information is required by 37 CFR 3.73(b). The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

Appendix A

Title: Multi-Platter Disk Drive Controller and Methods for Synchronous Redundant Data Operations
Serial No. 11/266,764
Filing date: 11/02/2005

Title: Intelligent Storage Engine for Disk Drive Operations with Reduced Local Bus Traffic
Serial No. 11/316,436
Filing date: 12/22/2005

Title: Disk Controller Methods and Apparatus with Improved Striping, Redundancy Operations and Interfaces
Serial No. 11/080,376
Filing date: 03/14/2005

Title: Disk Array Controller with Reconfigurable Data Path
Serial No. 10/829,918
Filing date: 04/21/2004

Title: Method and Apparatus for Synchronizing Data from Asynchronous Disk Drive Data Transfers
Serial No. 10/822,115
Filing date: 04/08/2004

Title: Raid Controller Disk Write Mask
Serial No. 11/060,884
Filing date: 02/17/2005

Title: Raid Controller Disk Write Mask
Serial No. 10/267,928 Filing date: 10/08/2002
Patent No. 6,904,498 Issue date: 06/07/2005

Title: ATA Emulation Host Interface in a RAID Controller
Serial No. 10/885,960
Filing date: 07/06/2004

Title: Disk Array Controller For Reading/Writing Striped Data Using A Single Address Counter For Synchronously Transferring Data Between Data Ports And Buffer Memory
Serial No. 08/642,453 Filing date: 5/3/1996
Patent No. 6,018,778 Issue date: 1/25/2000

Title: On-The-Fly Redundancy Operation For Forming Redundant Drive Data And Reconstructing Missing Data As Data Transferred Between Buffer Memory And Disk Drives During Write And Read Operation Respectively
Serial No. 09/411,698 Filing date: 10/1/1999
Patent No. 6,237,052 Issue date: 5/22/2001

Title: Raid Controller System and Method With ATA Emulation Host Interface
Serial No. 09/667,965 Filing date: 9/21/2000
Patent No. 6,772,108 Issue date: 8/3/2004

Title: On-The-Fly Redundancy Operation For Forming Redundant Drive Data And Reconstructing Missing Data As Data Transferred Between Buffer Memory And Disk Drives During Write And Read Operation Respectively
Serial No. 10/445,396
Filing date: 05/22/2003